

Fig. 1 Typical circuit configuration.

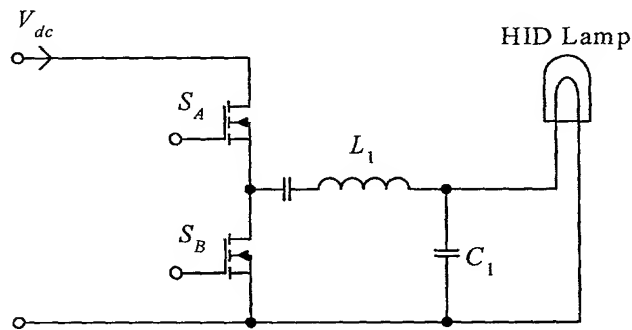


Fig. 2 Lamp network using L-C circuit. (Prior Art)

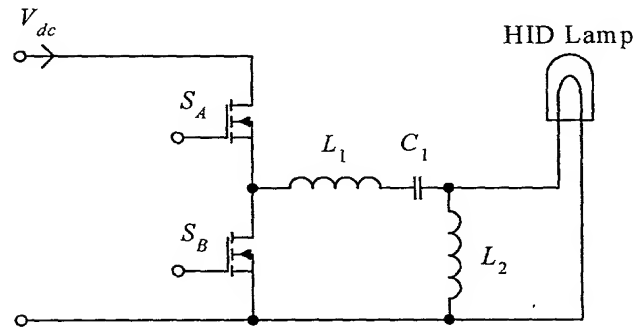


Fig. 3 Lamp network using L-C-L circuit. (Prior Art)

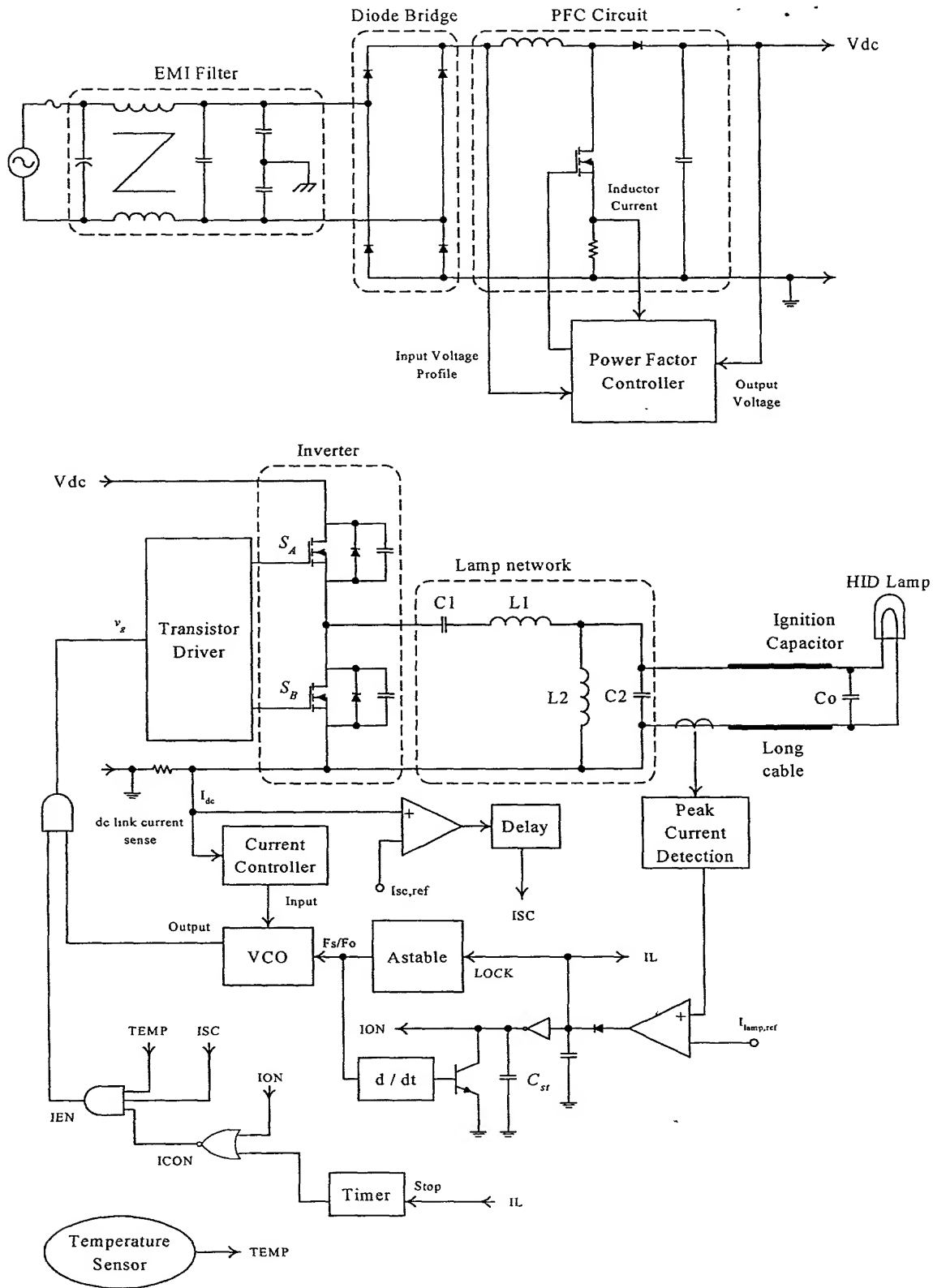


Fig. 4 Schematic diagram of the proposed ballast.

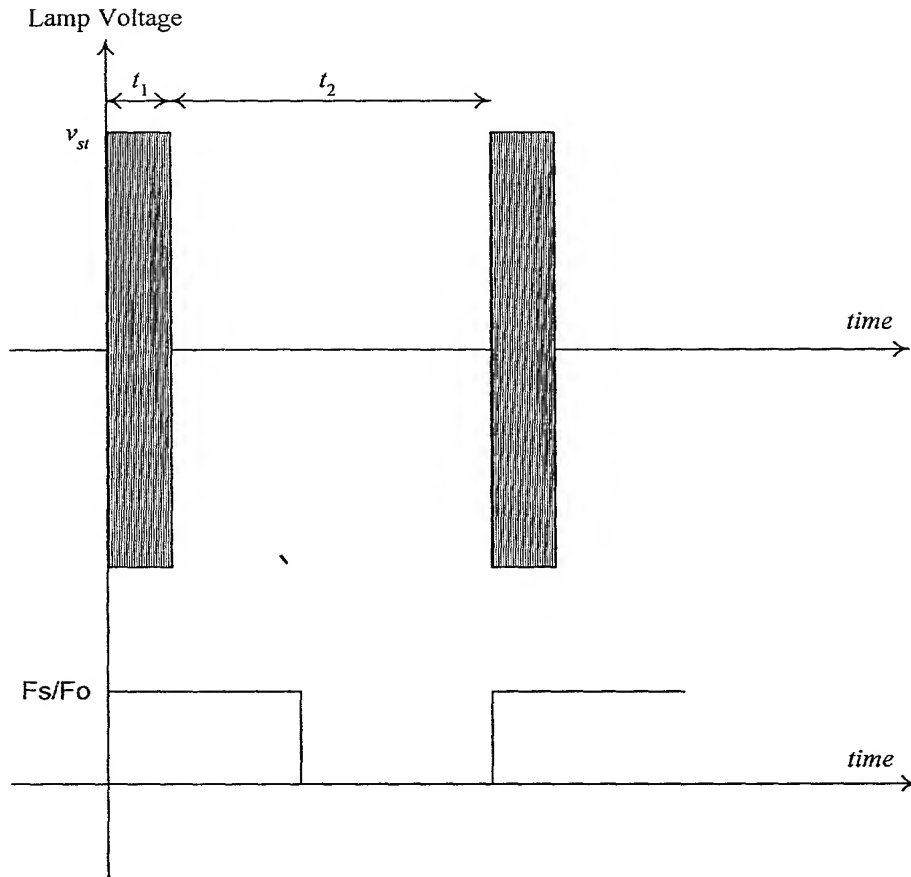


Fig. 4 Typical waveforms of the lamp voltage and Fs/Fo signal during the start-up process with root-mean-square voltage limiting feature.